

Fig. 1

FIG. 2A is a block diagram of a communication system 200A.

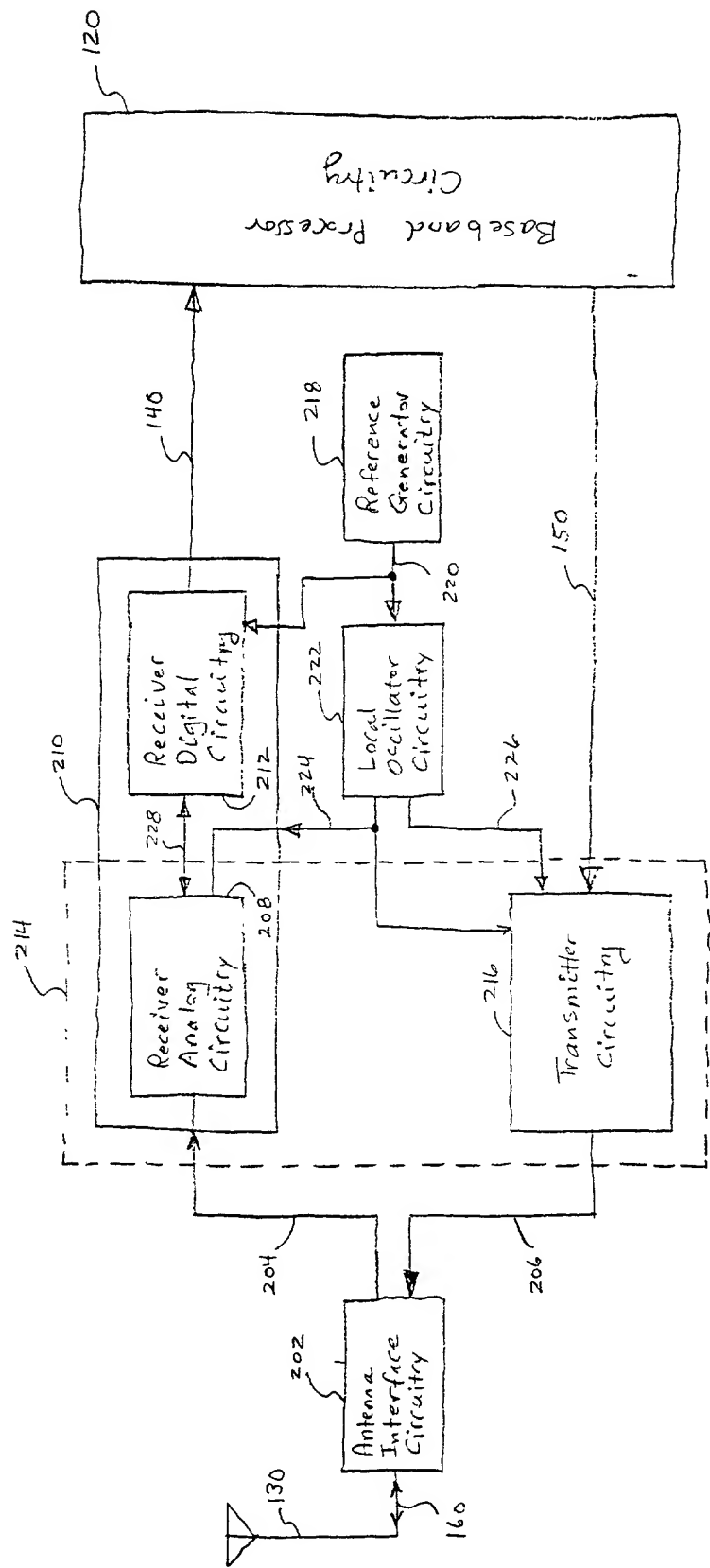


FIG. 2A

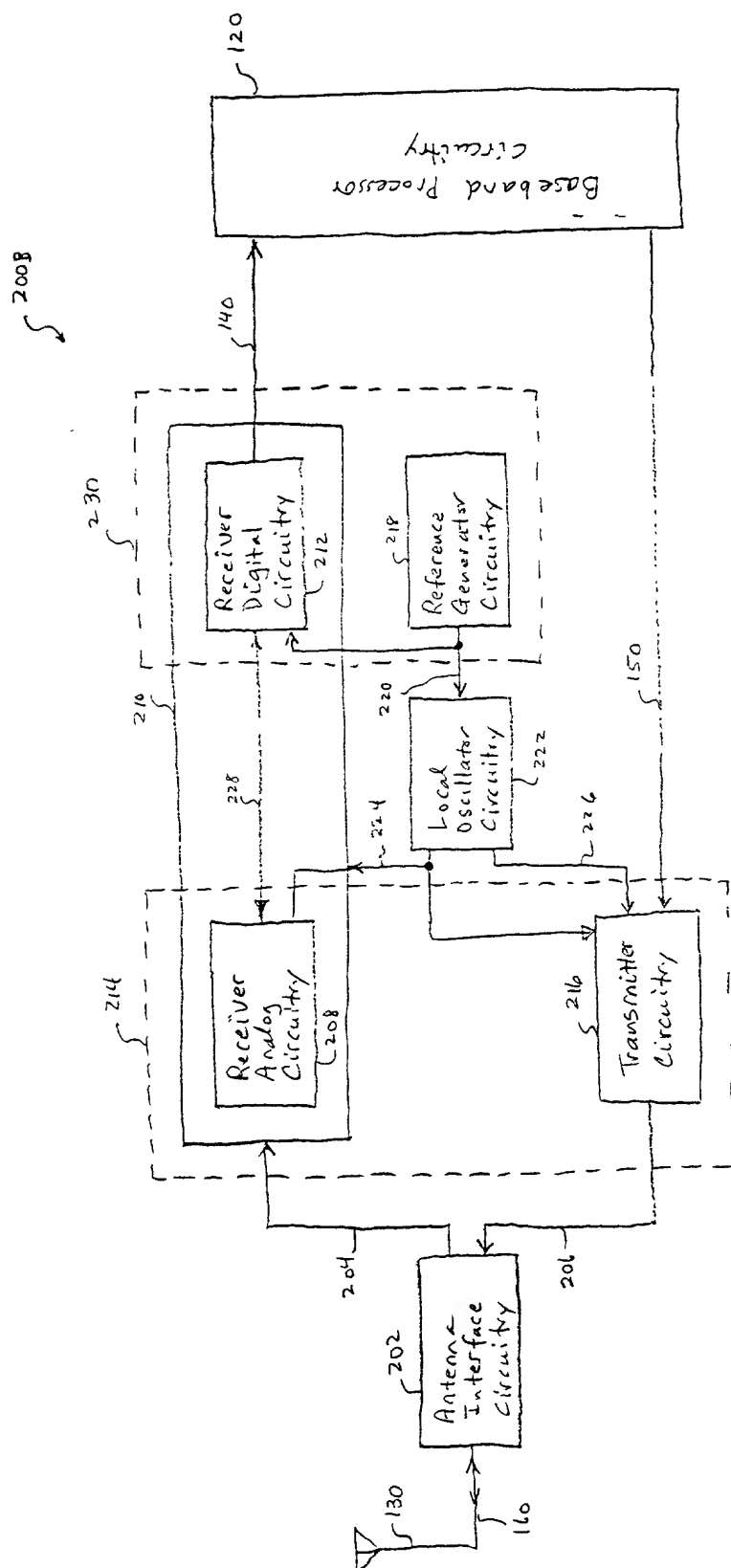


FIG. 2B

200C

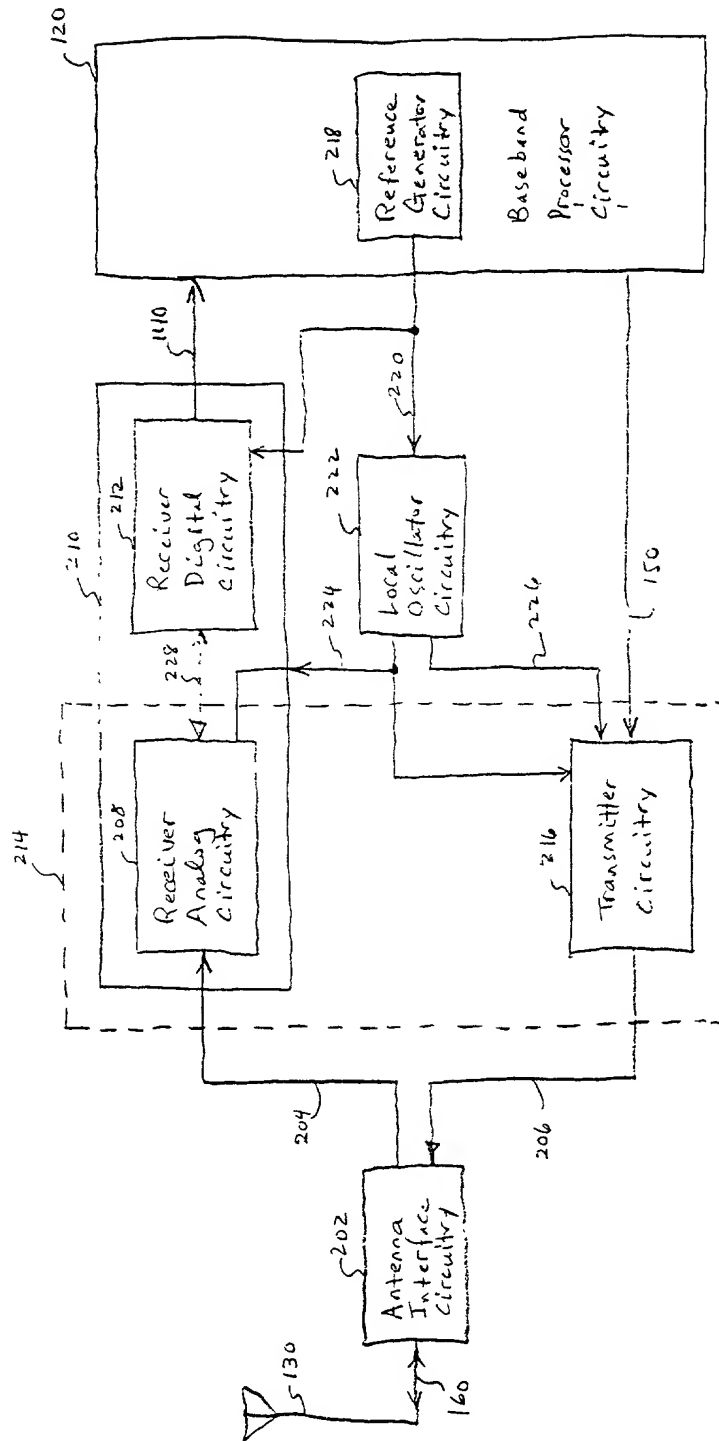


FIG. 2C

200D

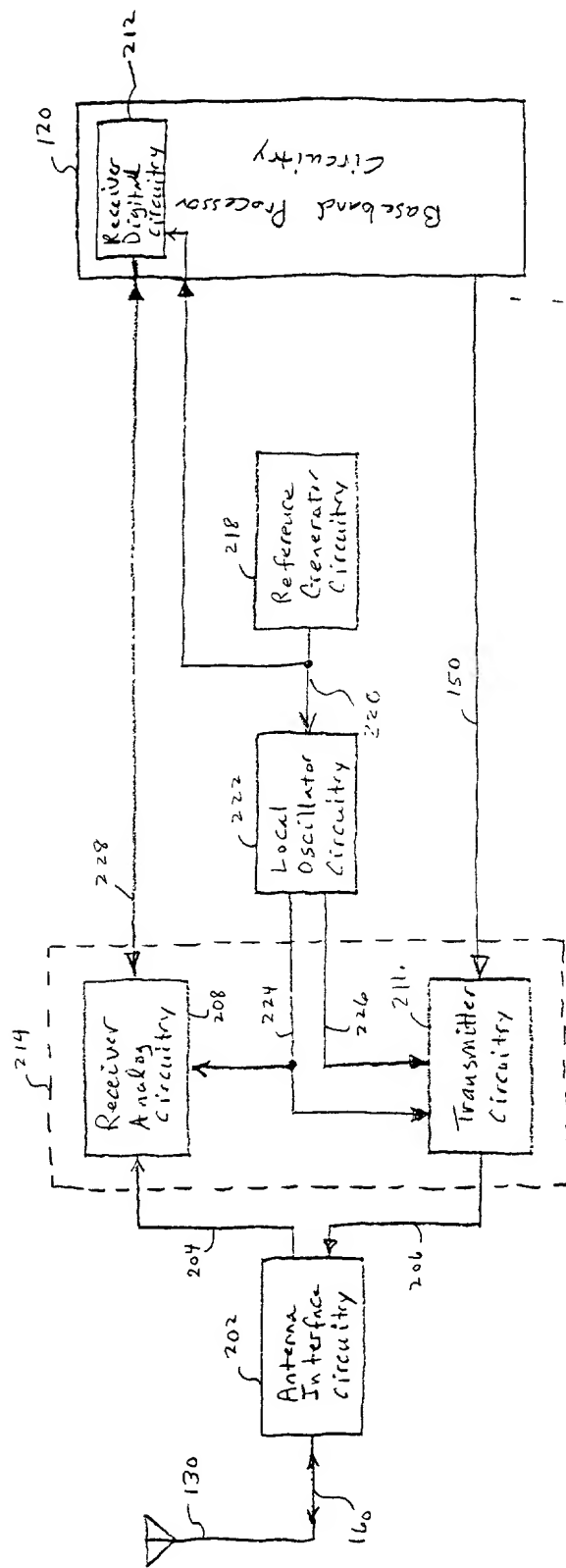


FIG. 2D

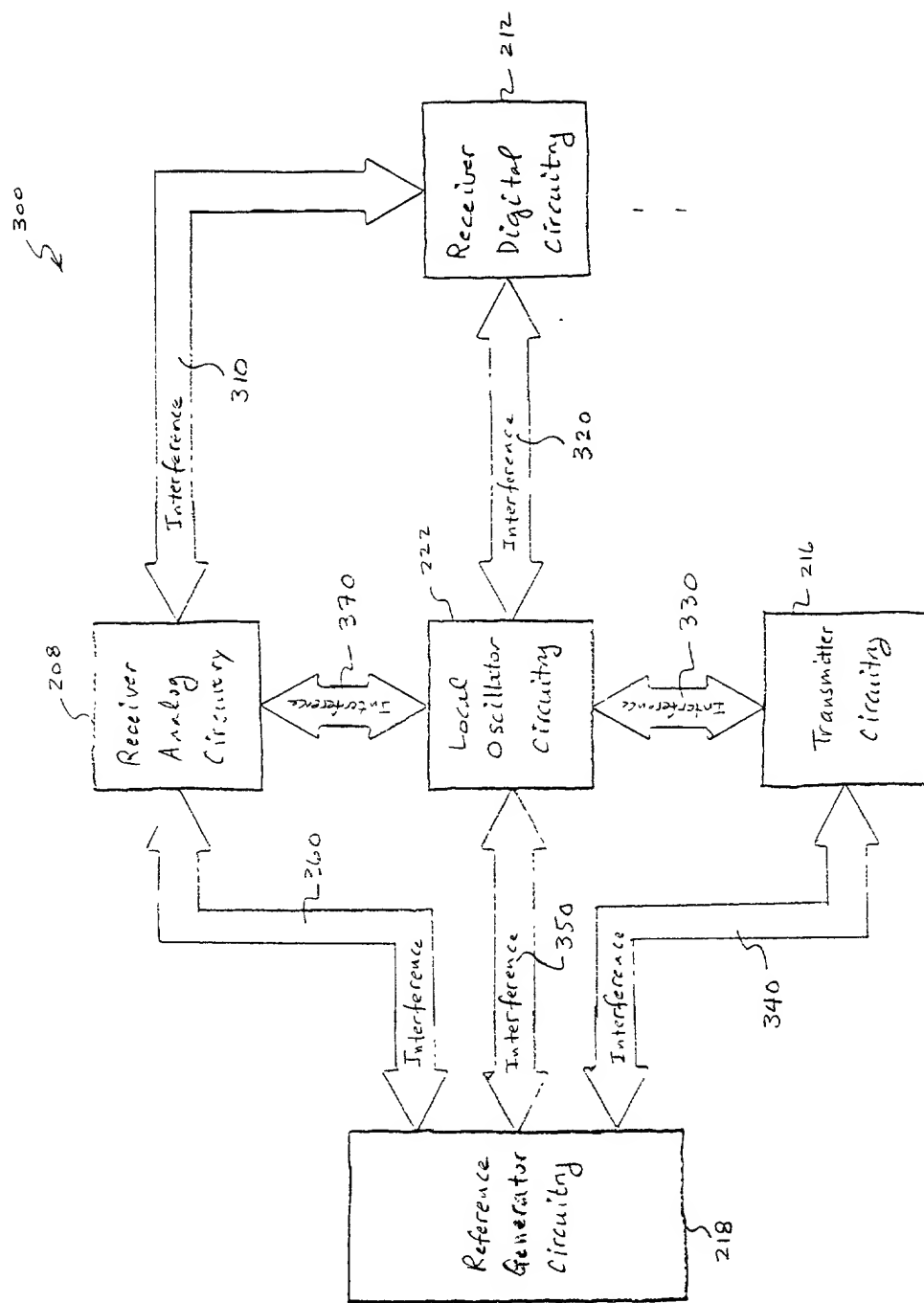


FIG. 3

FIG. 4 is a block diagram of a communication system 400, which includes an antenna 130, an antenna interface circuit 202, a baseband processor 120, and a baseband circuit 407. The antenna interface circuit 202 is connected to the antenna 130 and the baseband processor 120. The baseband processor 120 is connected to the baseband circuit 407. The baseband circuit 407 includes a filter circuit 403, a down-converter circuit 409, an ADC circuit 412, a DAC circuit 445, a digital filter circuit 439, a local oscillator circuit 222, a reference generator circuit 218, a PLL circuit 472, a gate-up-converter circuit 467, a transmit circuit 475, and a power amplifier circuit 487. The baseband circuit 407 is connected to the baseband processor 120 and the antenna interface circuit 202. The baseband circuit 407 is connected to the baseband processor 120 and the antenna interface circuit 202. The baseband circuit 407 is connected to the baseband processor 120 and the antenna interface circuit 202.

400

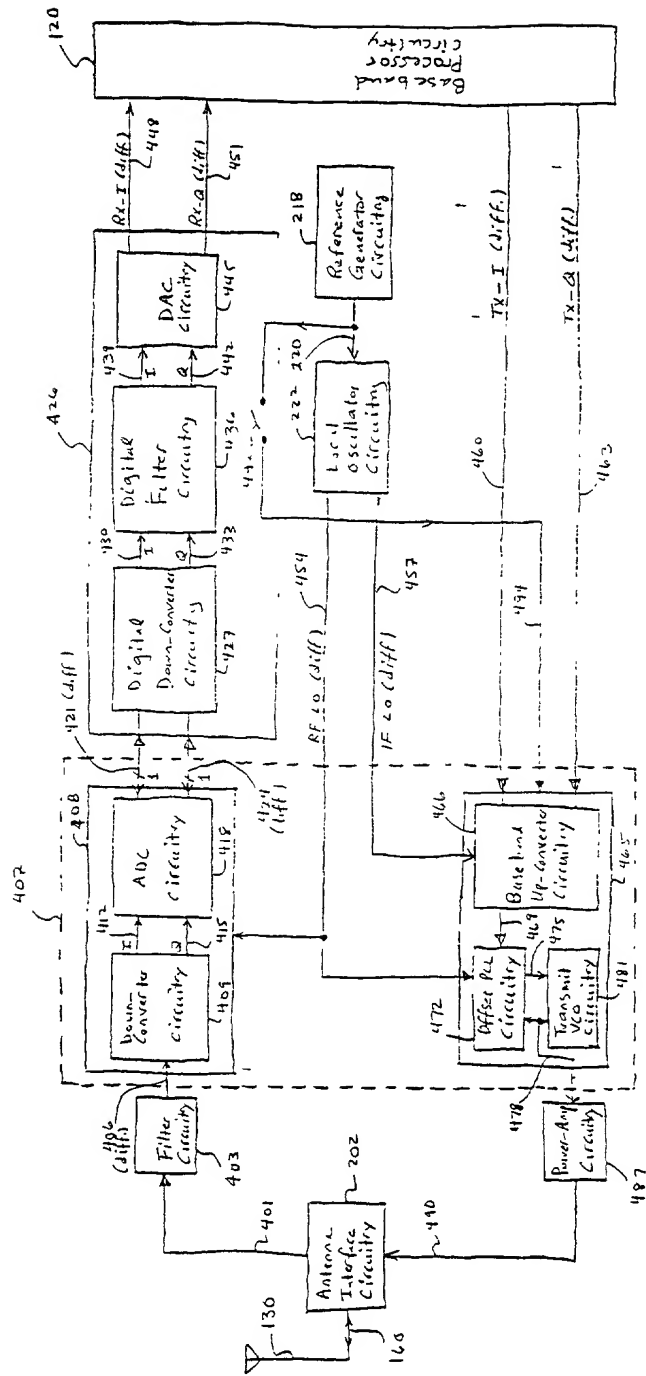


FIG. 4

500

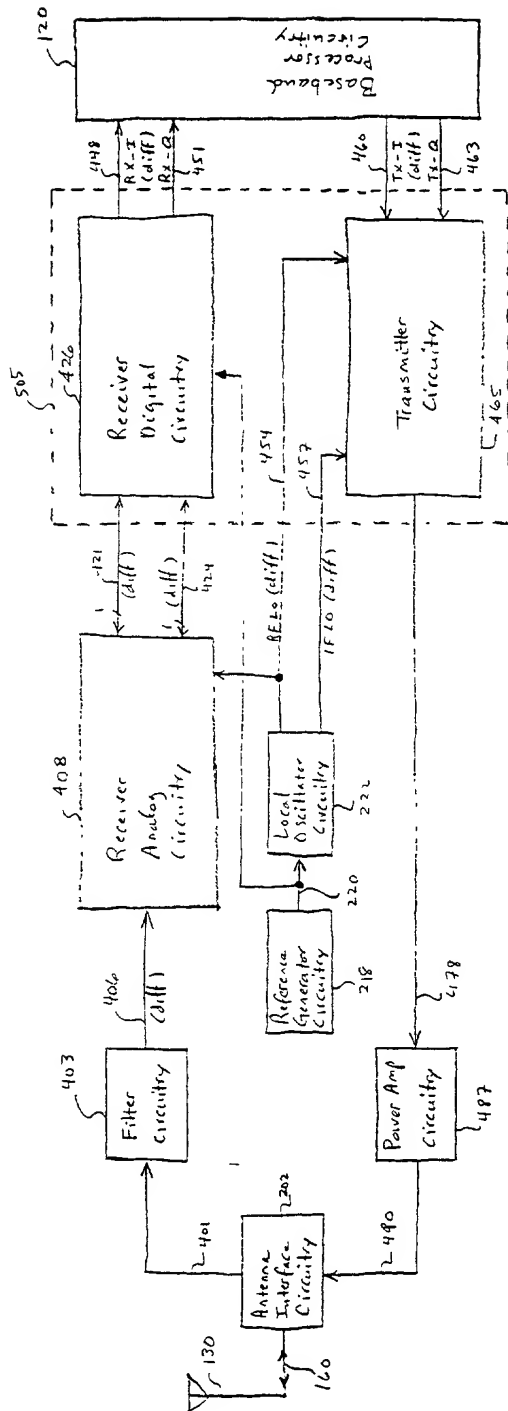


FIG. 5



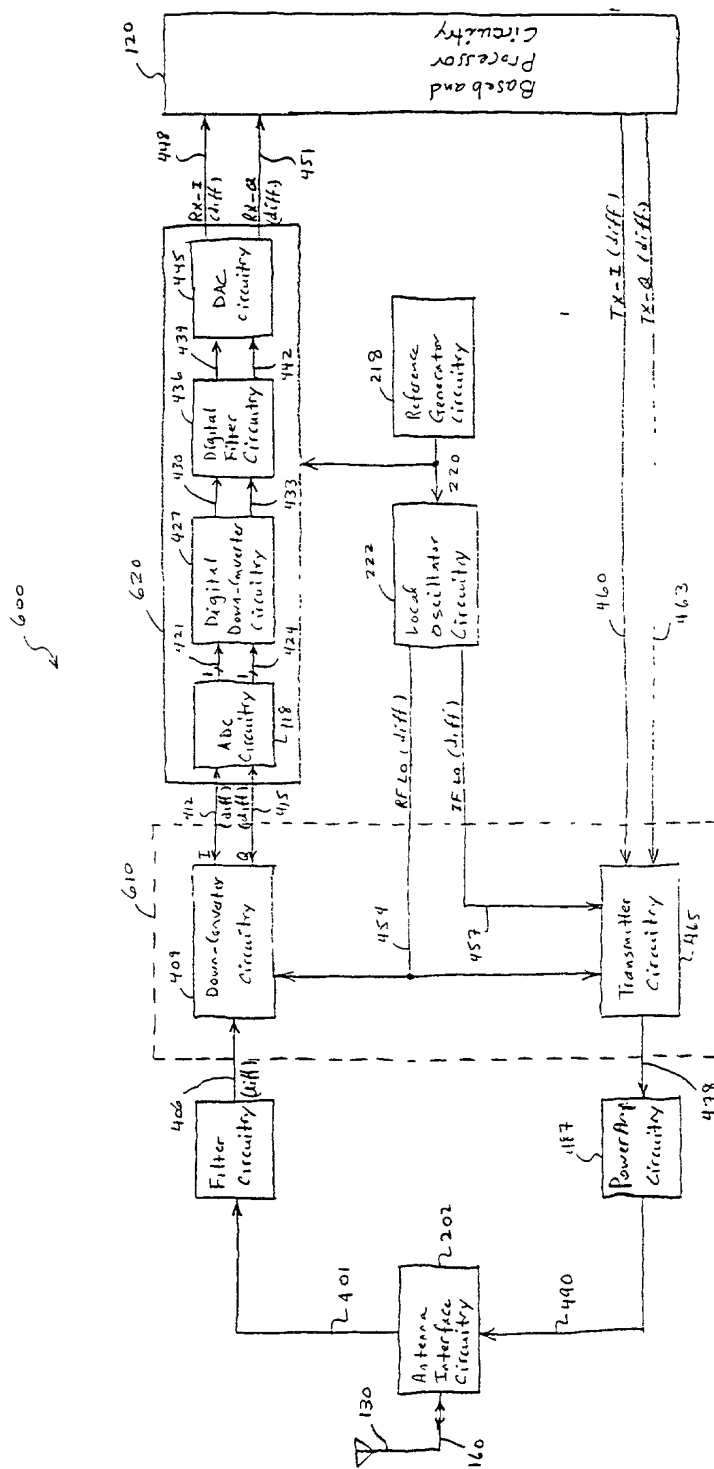


FIG. 6

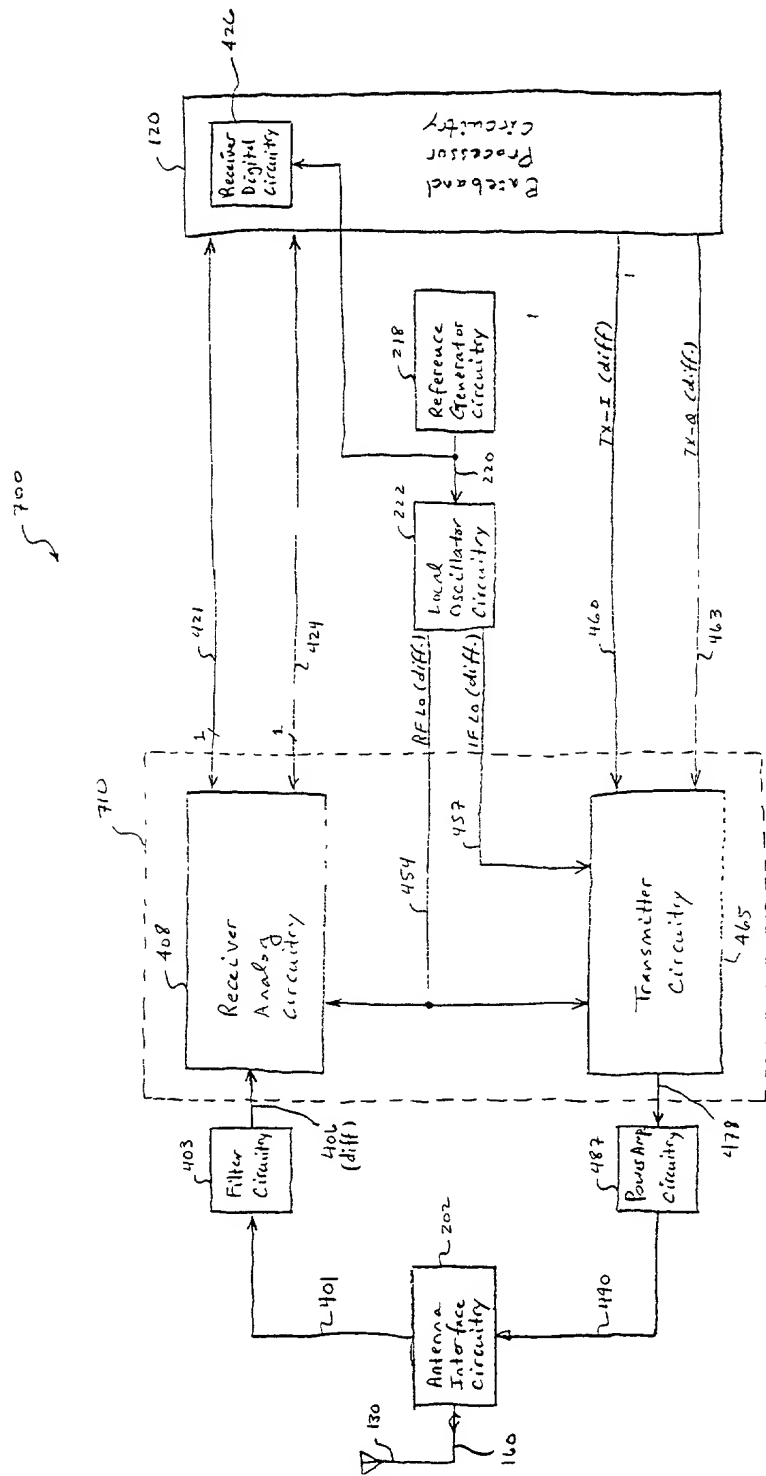


FIG. 7

800

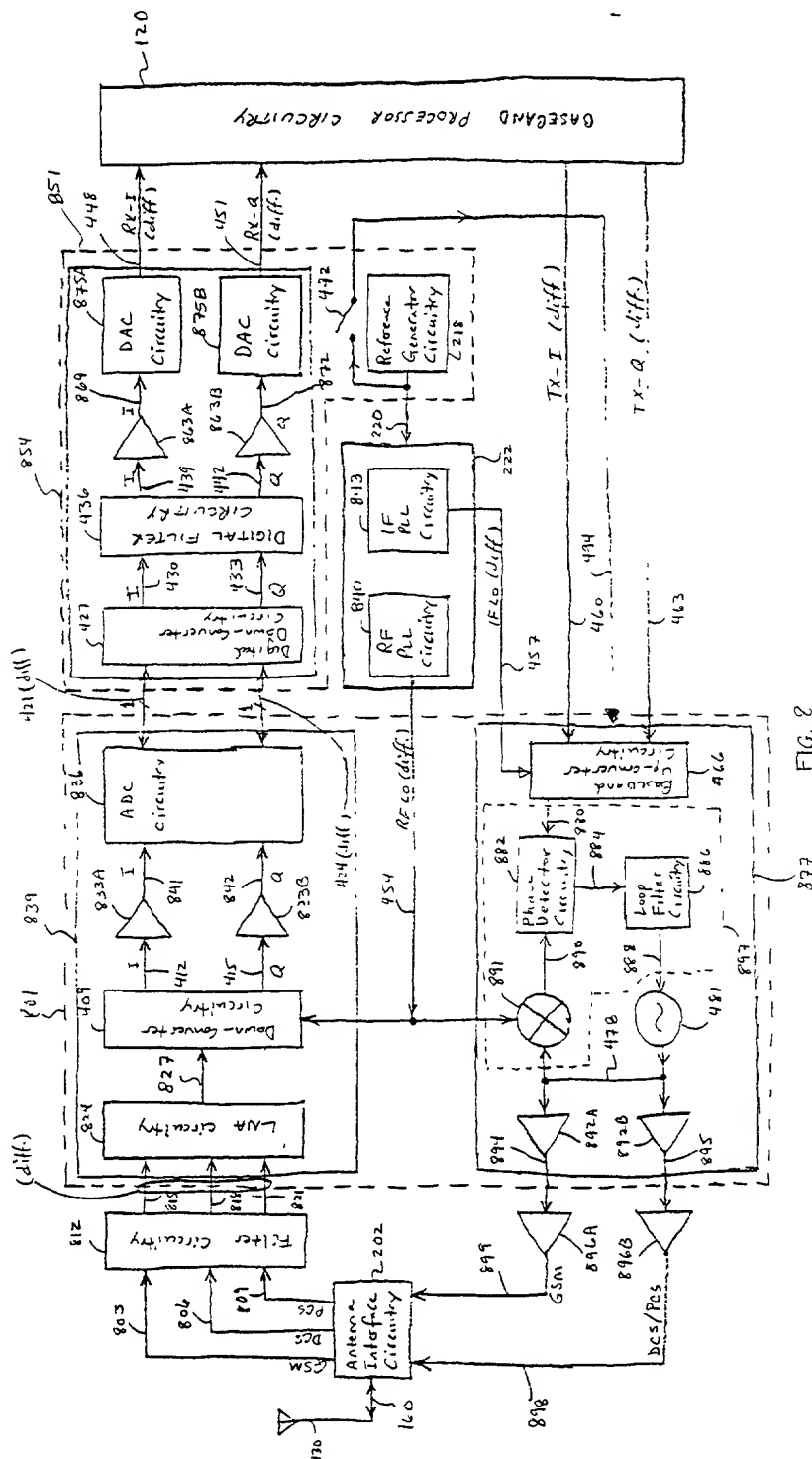


FIG. 2

FIG. 9A is a block diagram of a receiver system. The system includes a Baseband Receiver Circuit (120), a Receiver Digital Circuitry (906), and a Receiver Analog Circuitry (916). The Baseband Receiver Circuit (120) receives signals from an antenna (110) and outputs signals to the Receiver Digital Circuitry (906). The Receiver Digital Circuitry (906) outputs signals to the Receiver Analog Circuitry (916), which then outputs signals to a processor (945). The Receiver Digital Circuitry (906) also receives signals from a processor (945). The Receiver Analog Circuitry (916) receives signals from a processor (945) and outputs signals to an antenna (915). The Receiver Digital Circuitry (906) also receives signals from an antenna (915). The Receiver Analog Circuitry (916) also receives signals from an antenna (915). The Receiver Digital Circuitry (906) also receives signals from an antenna (915). The Receiver Analog Circuitry (916) also receives signals from an antenna (915).

FIG. 9A

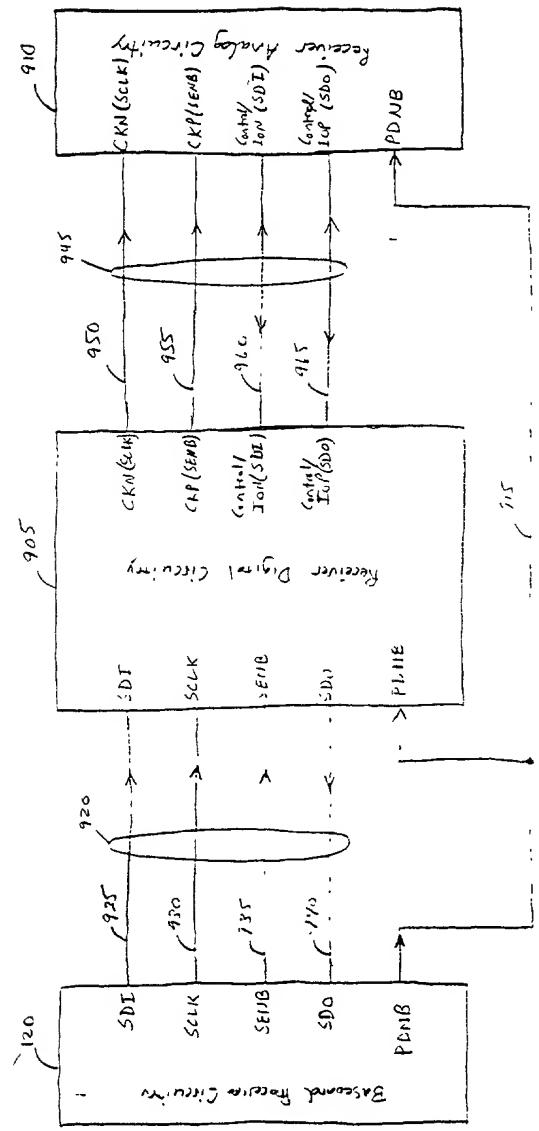


FIG. 9A

900B

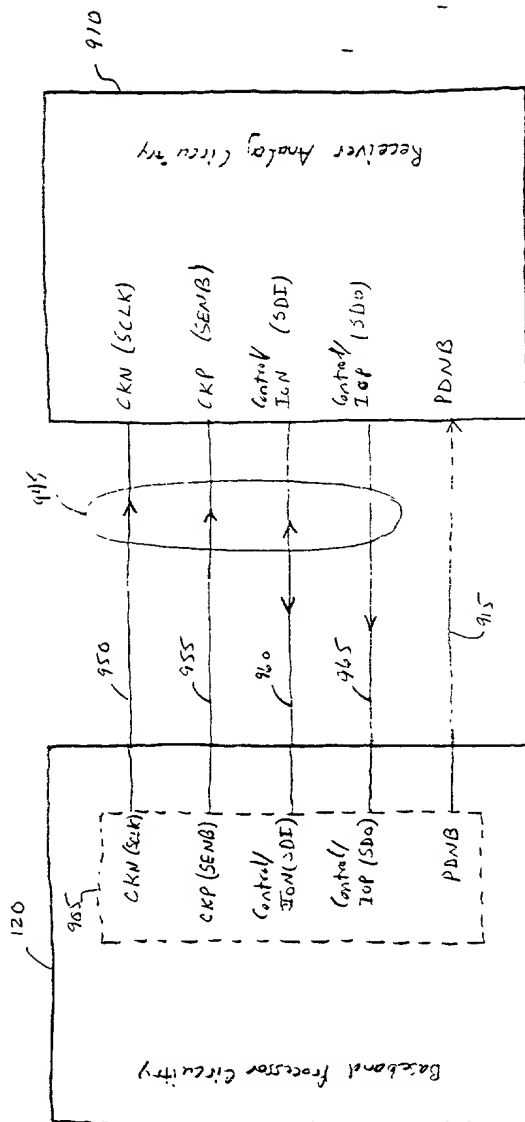


FIG. 9B

FIG. 10 is a block diagram of the receiver circuitry of the system, showing the connection between the baseband receiver circuitry and the receiver digital circuitry.

1000

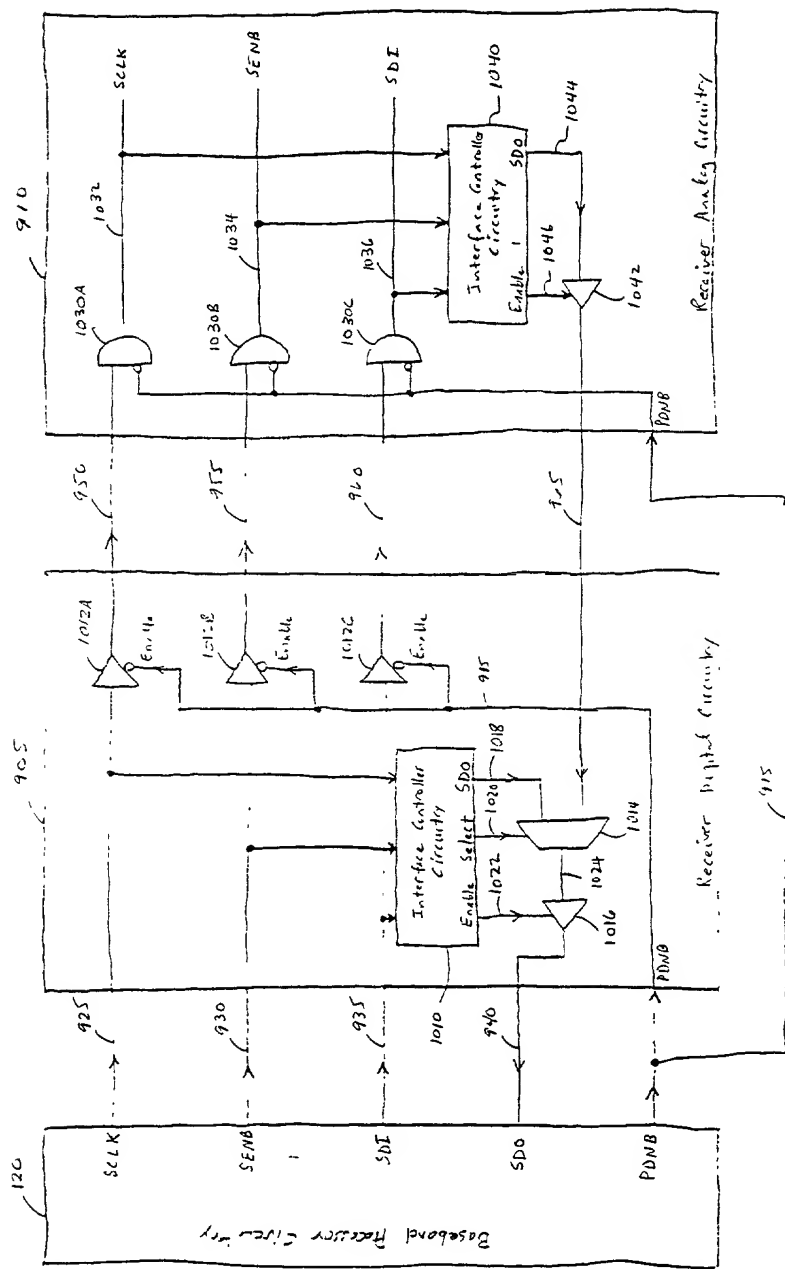


FIG. 10



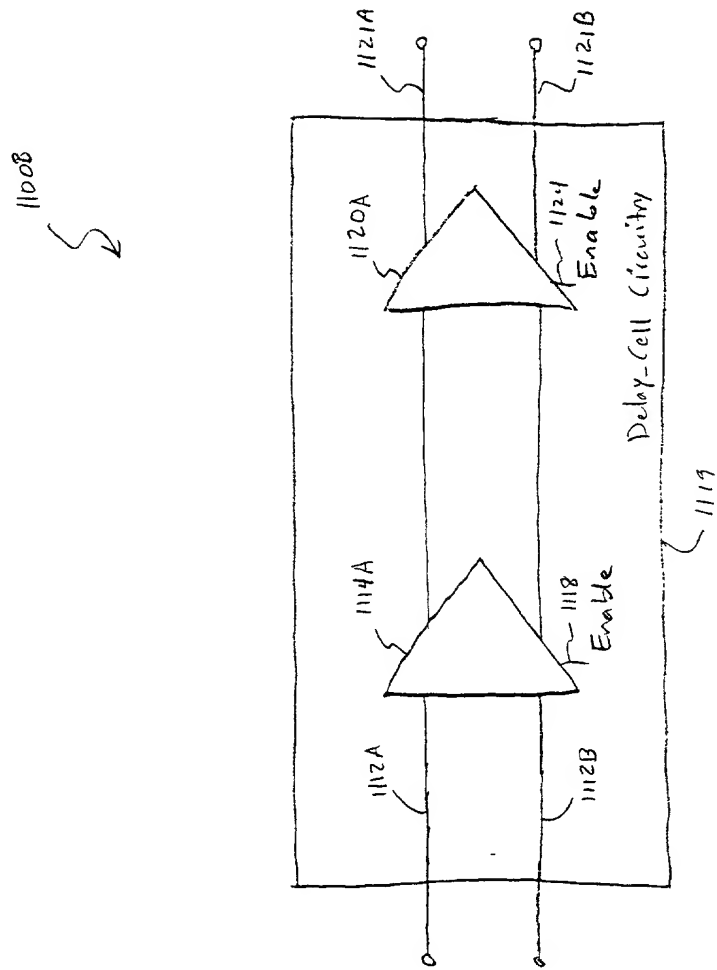


FIG. 11B



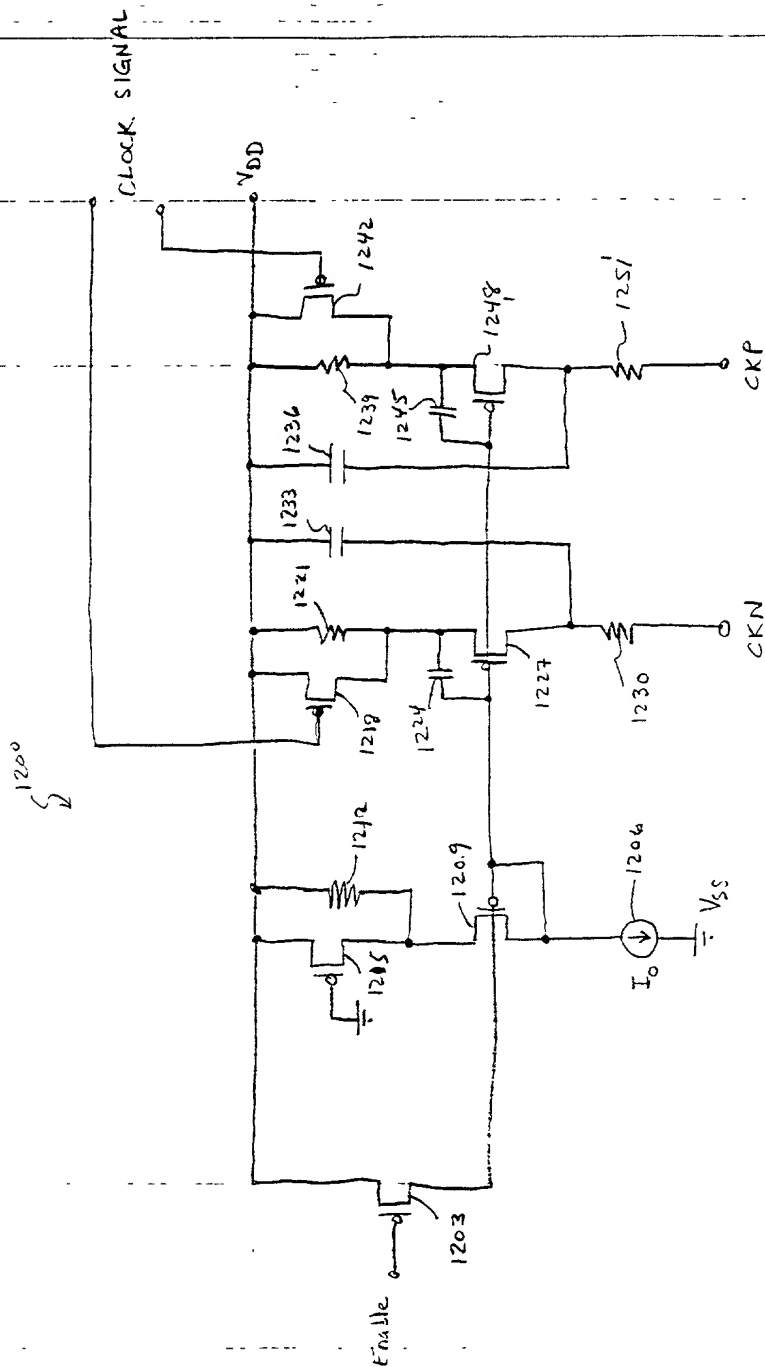


FIG. 12

Handwritten text at the top of the page, likely a title or reference number, is partially legible and appears to be "FIG. 13A".

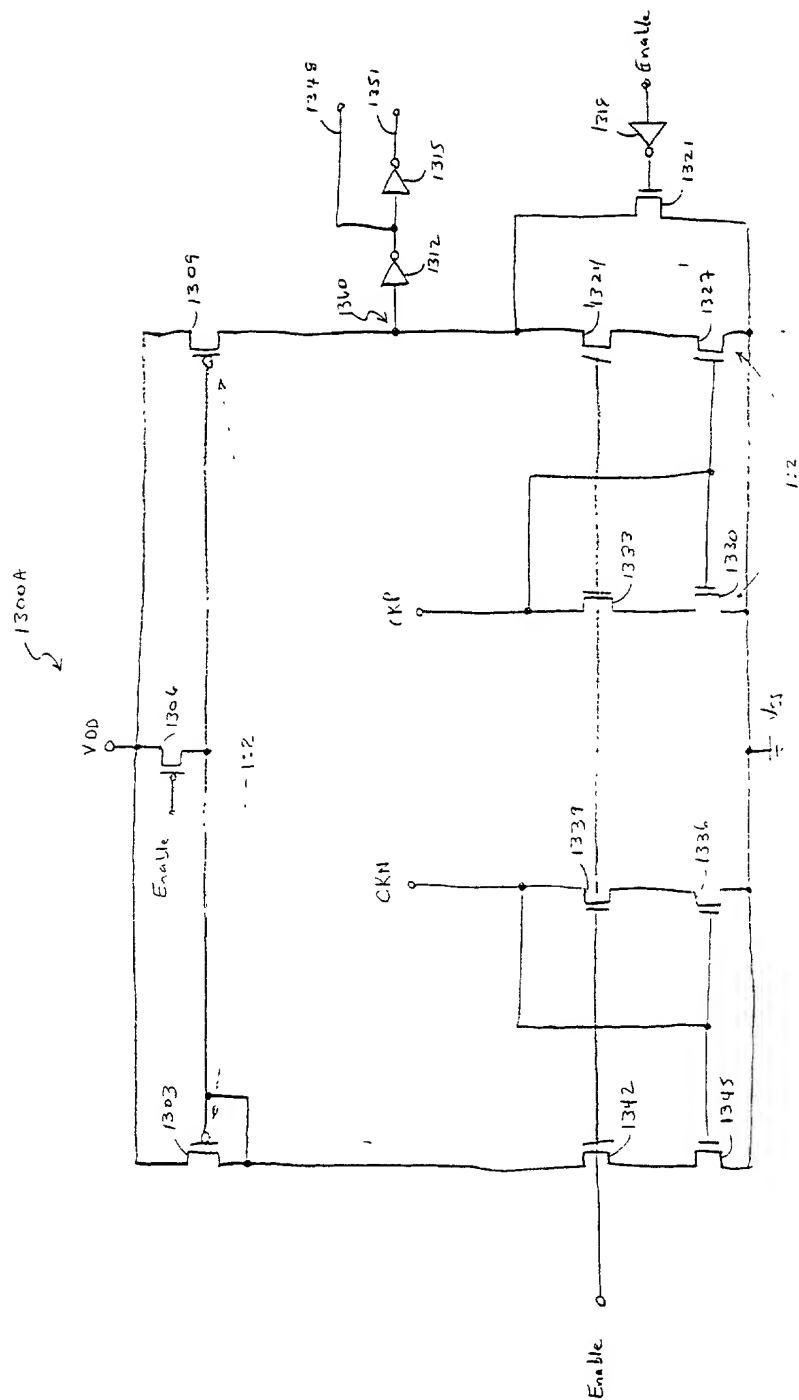


FIG. 13A

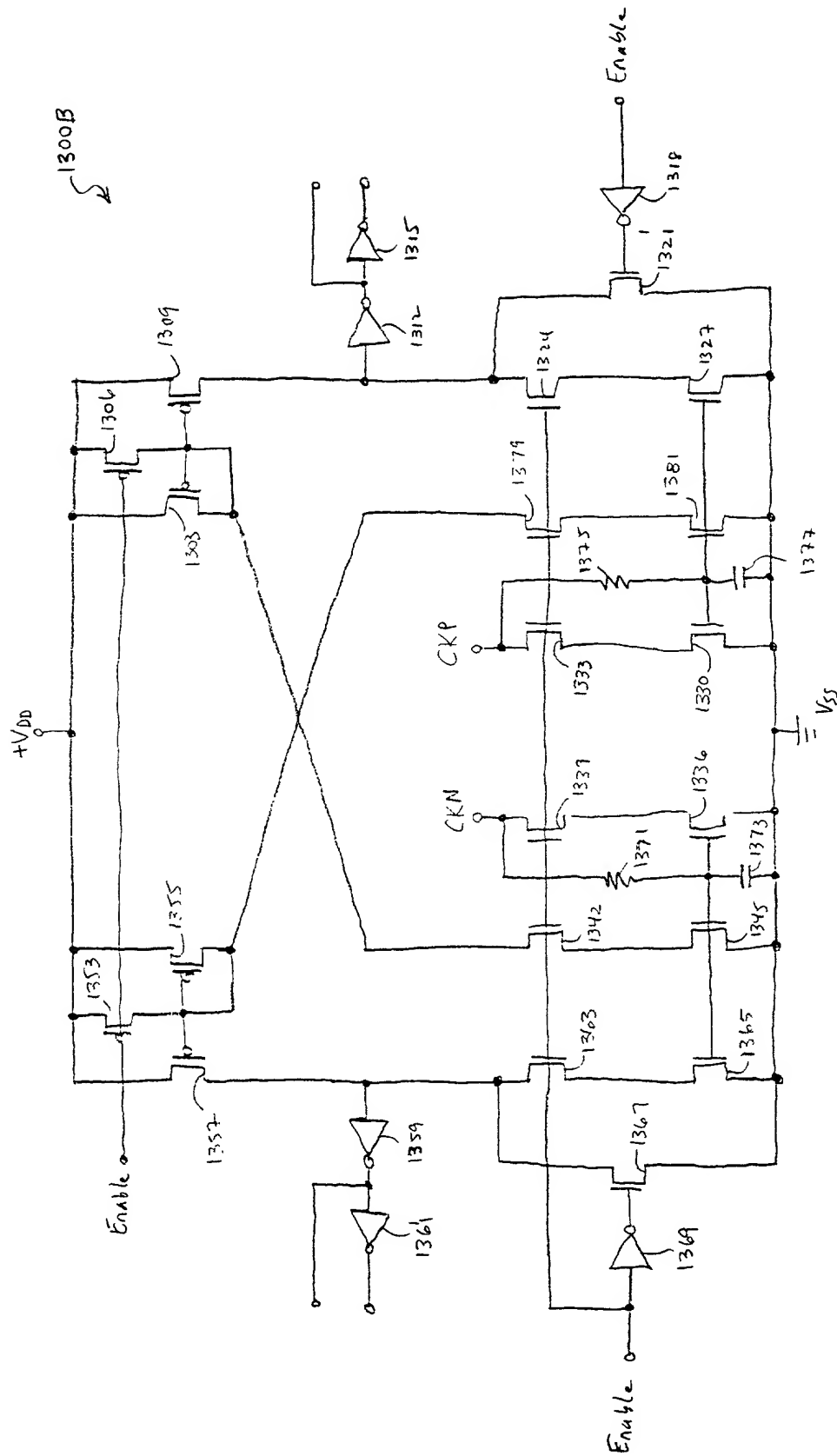


FIG. 13B

FIG. 14

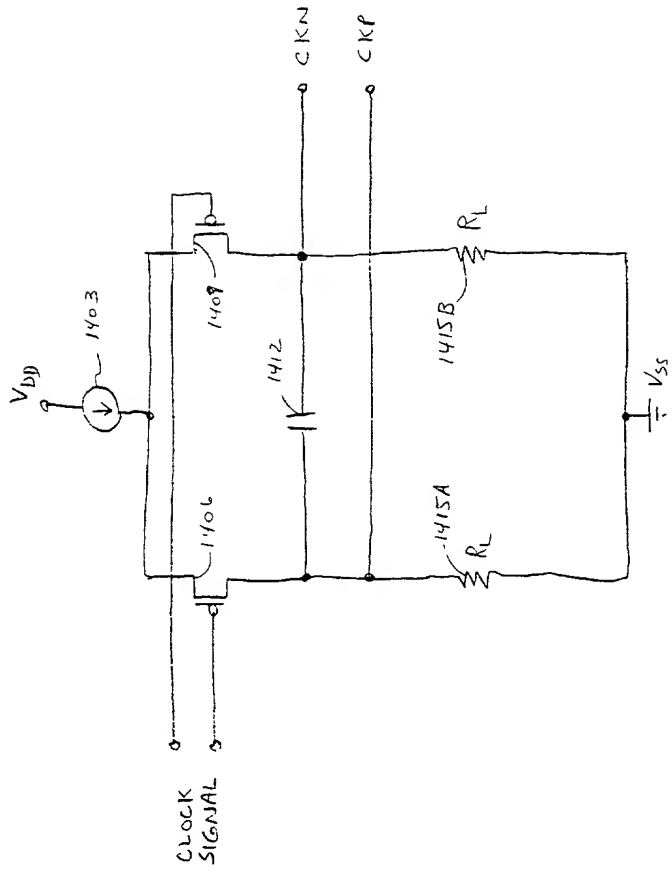


FIG. 14

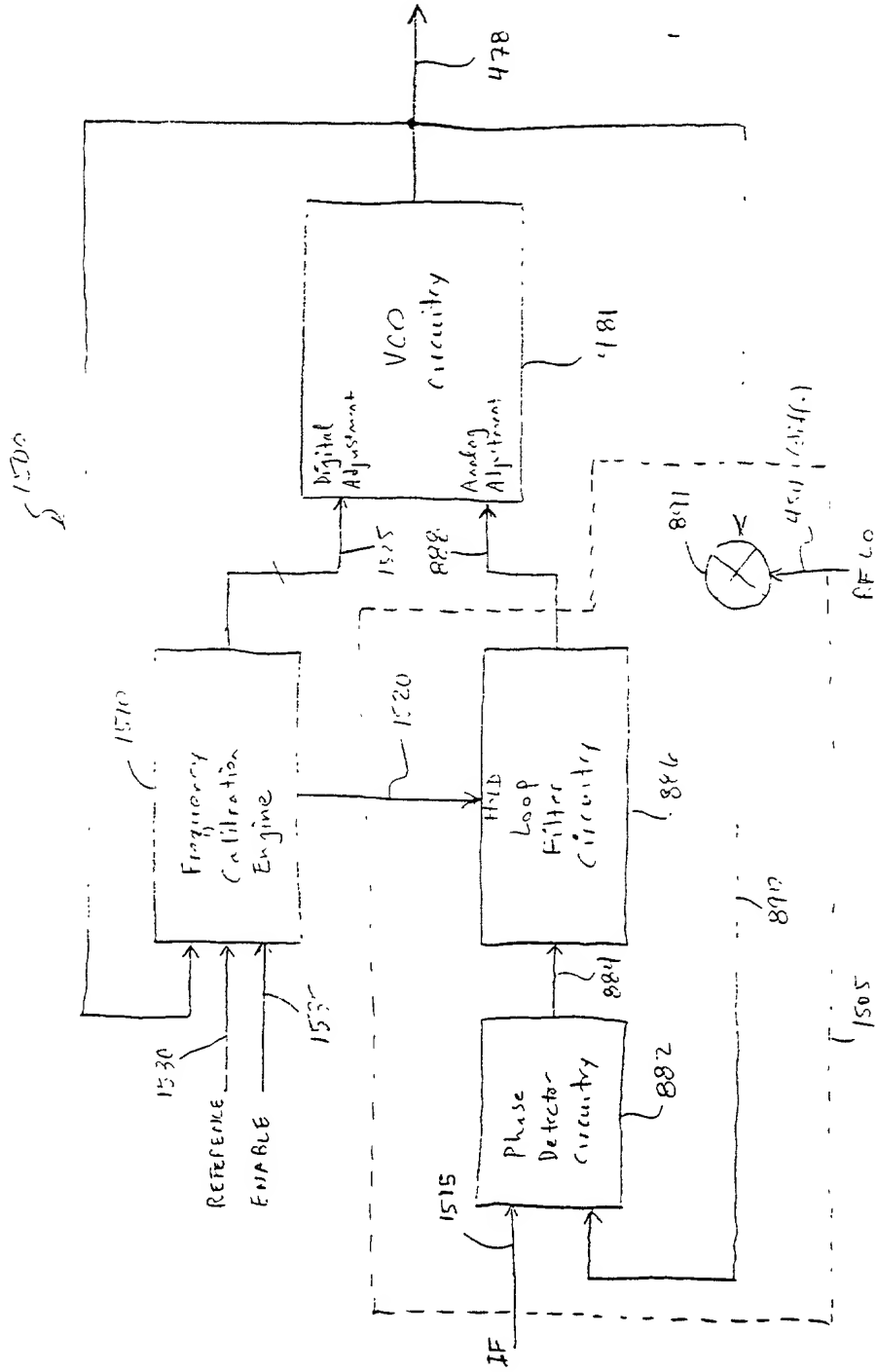
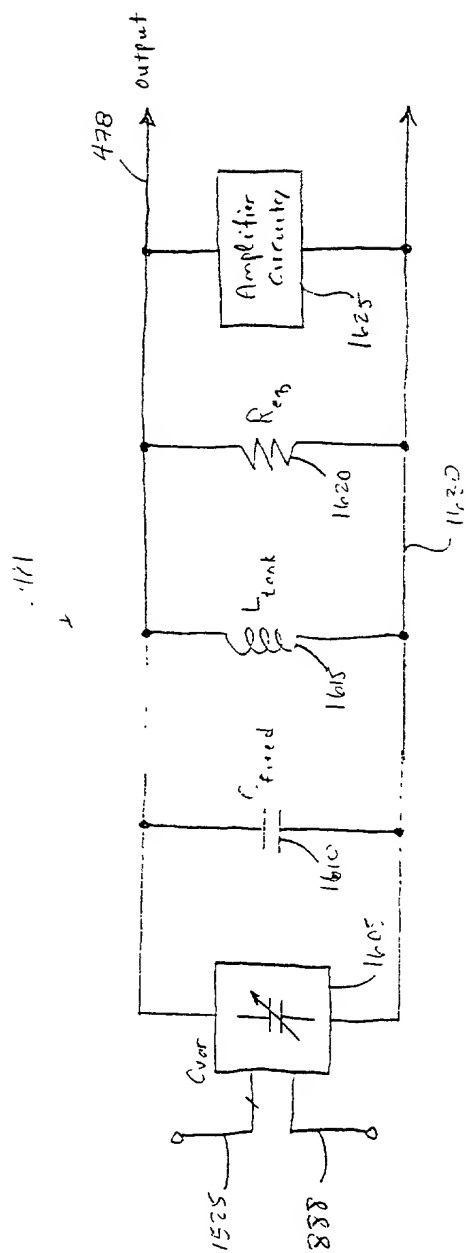


FIG. 15



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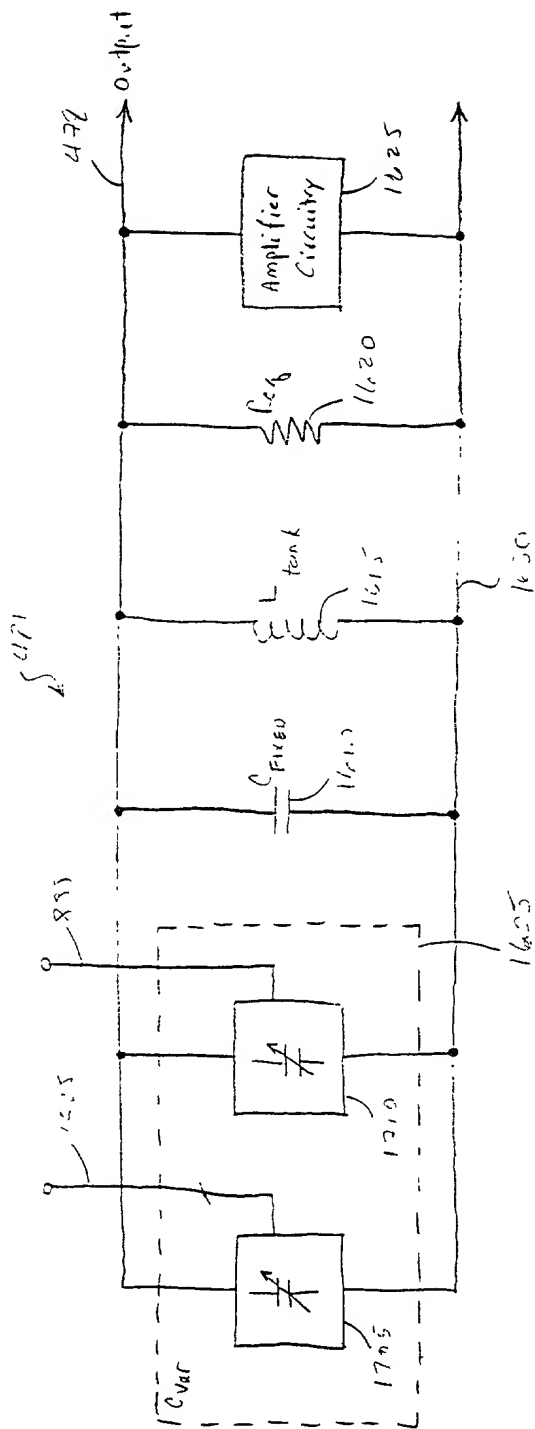
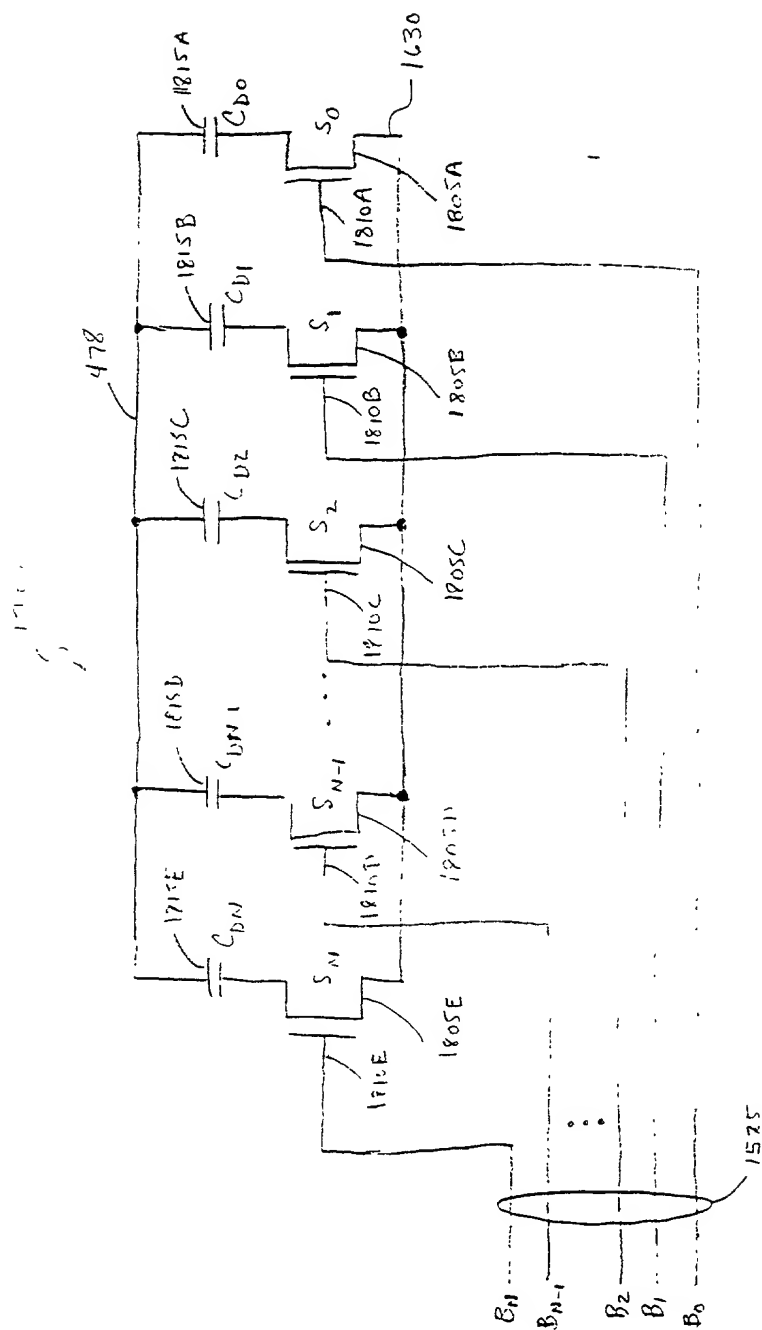


FIG. 17





1900A

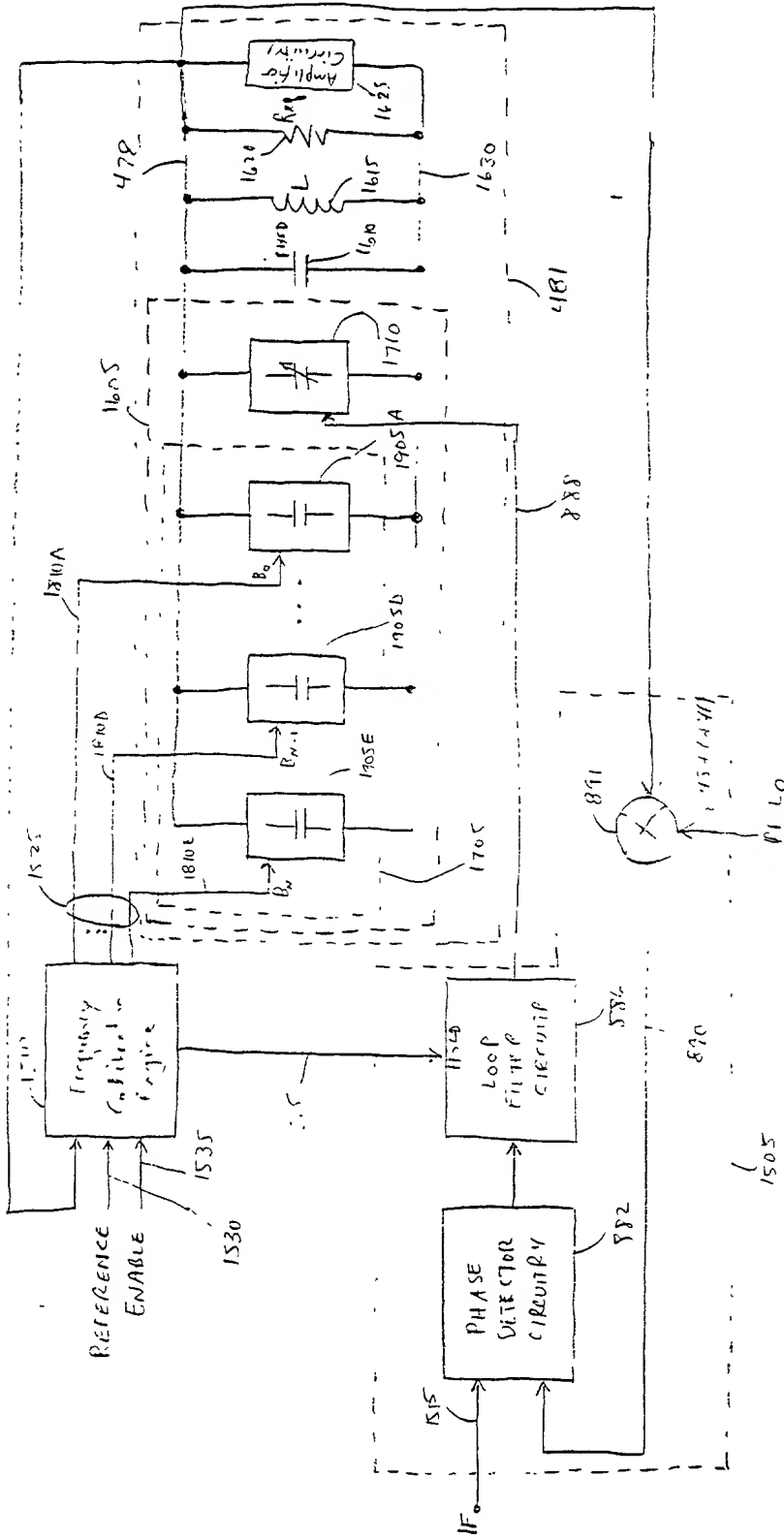


FIG. 19A

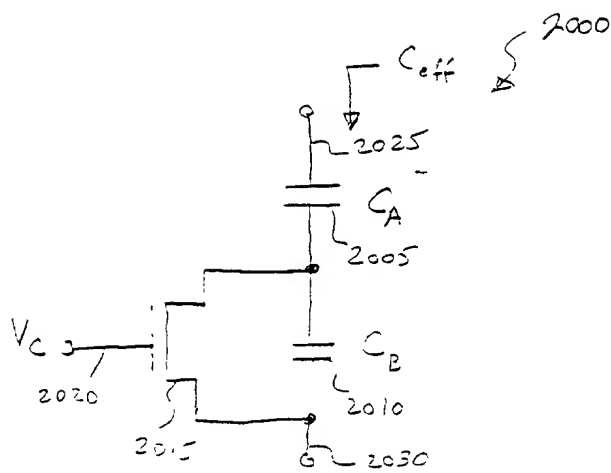


FIG. 20

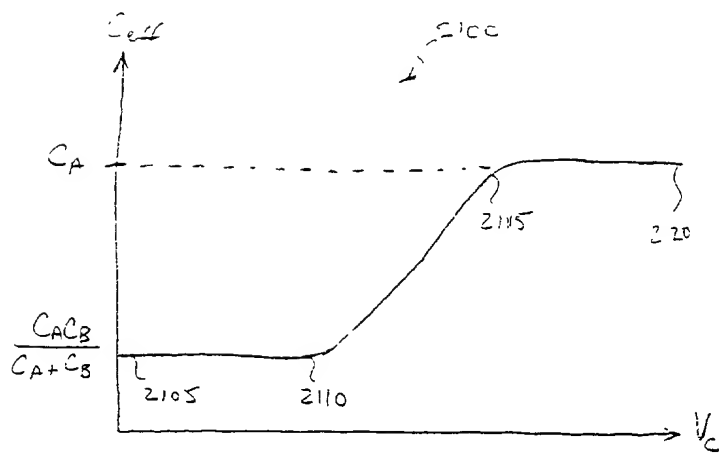


FIG. 21

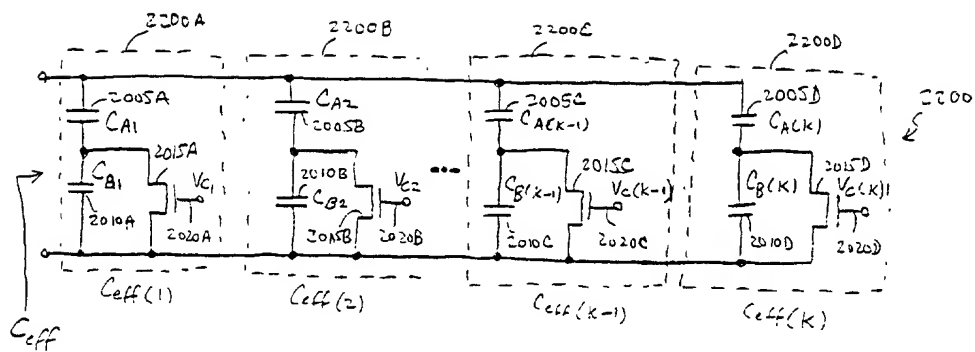


FIG. 22

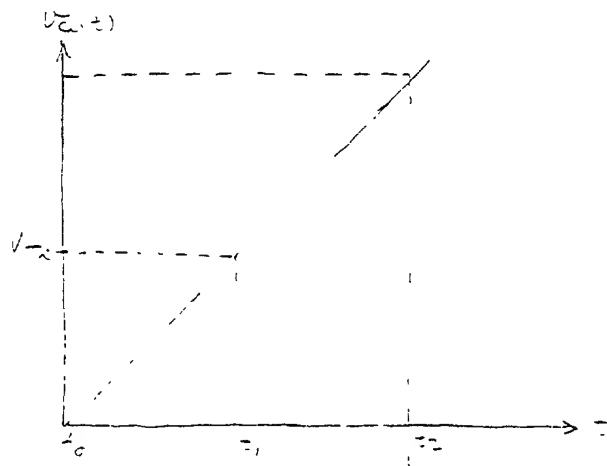


FIG. 22A

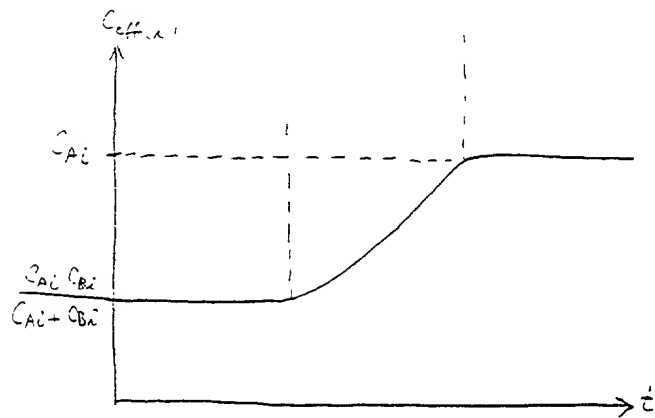


FIG. 22B

FIG. 24A

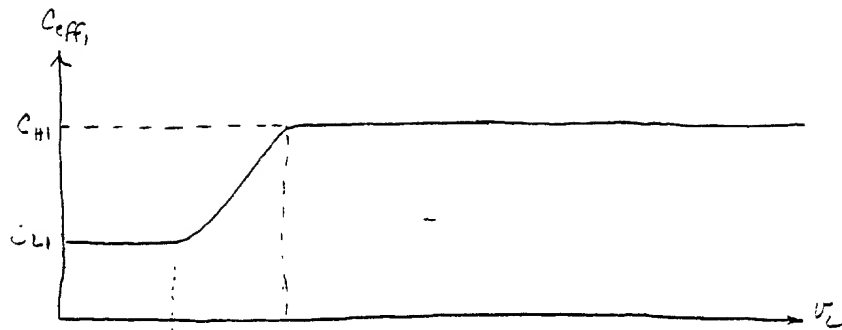


FIG. 24B

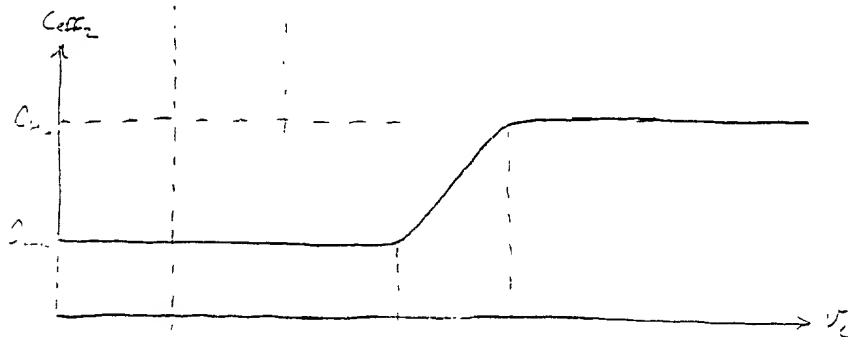


FIG. 24C

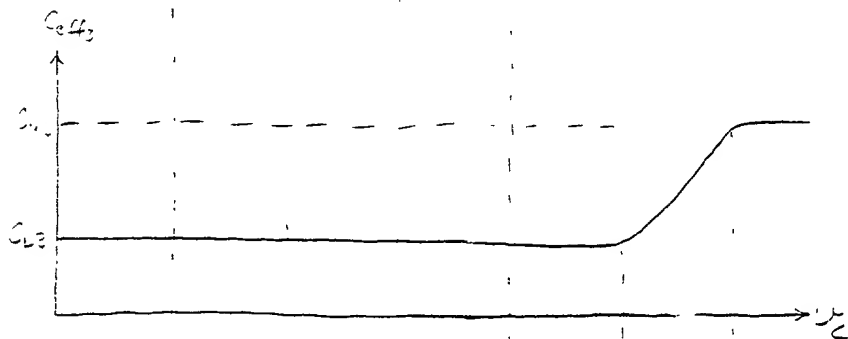
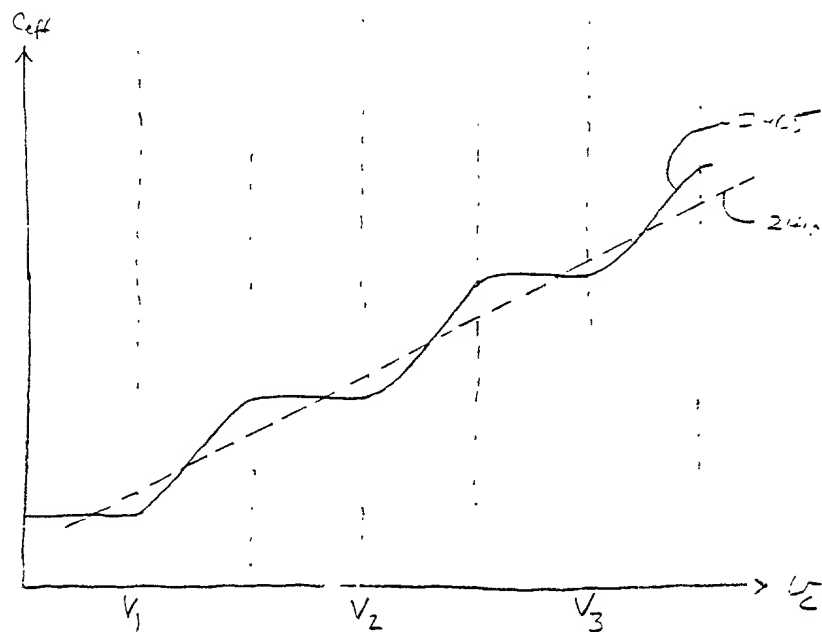


FIG. 24D



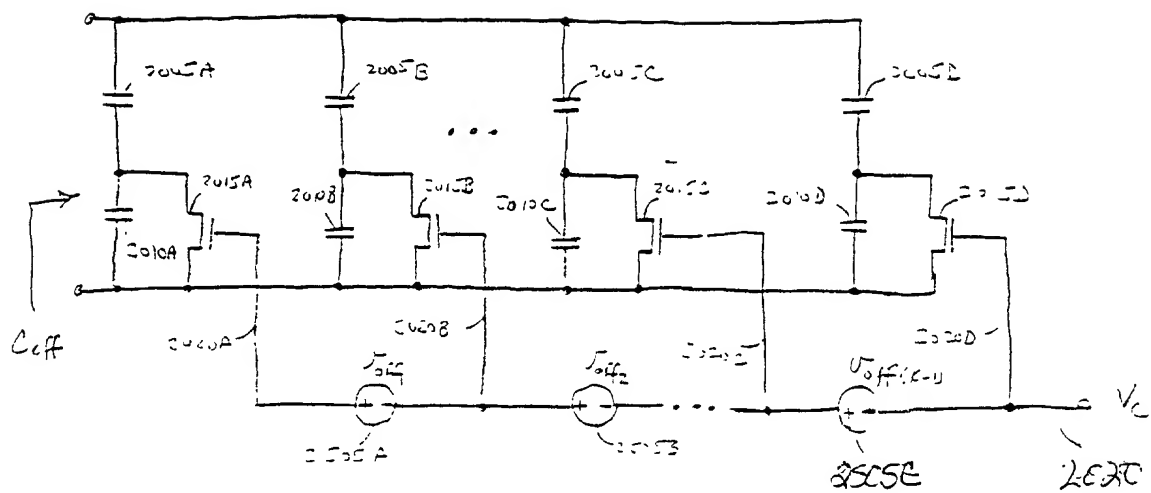


FIG. 25

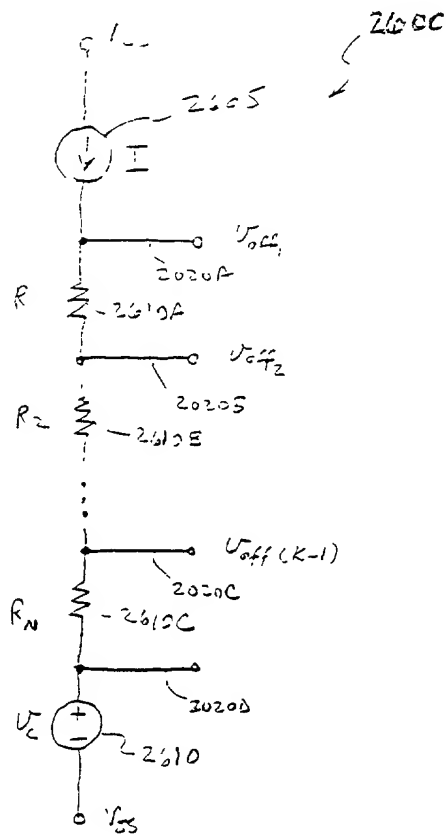


FIG. 26



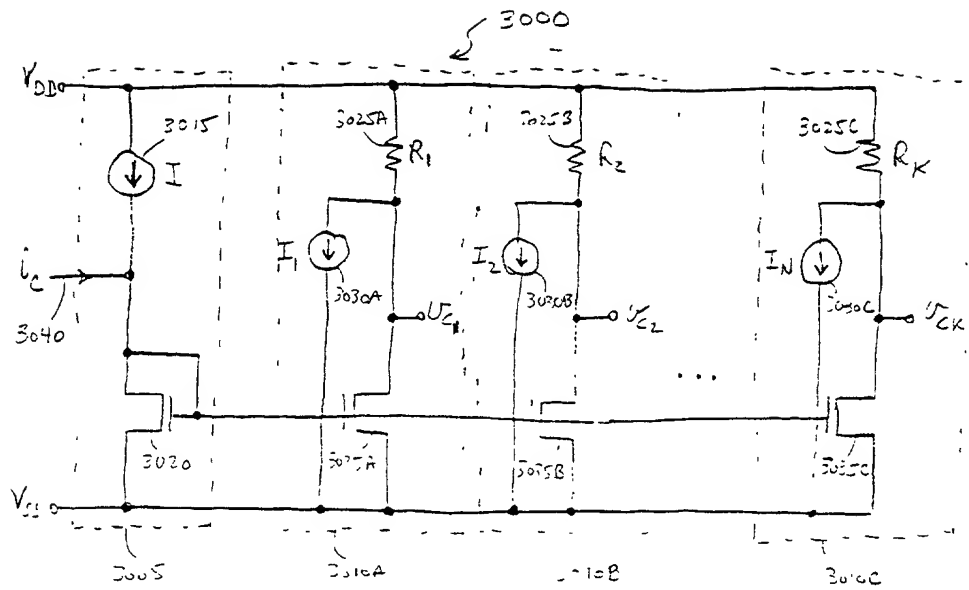


FIG. 3C

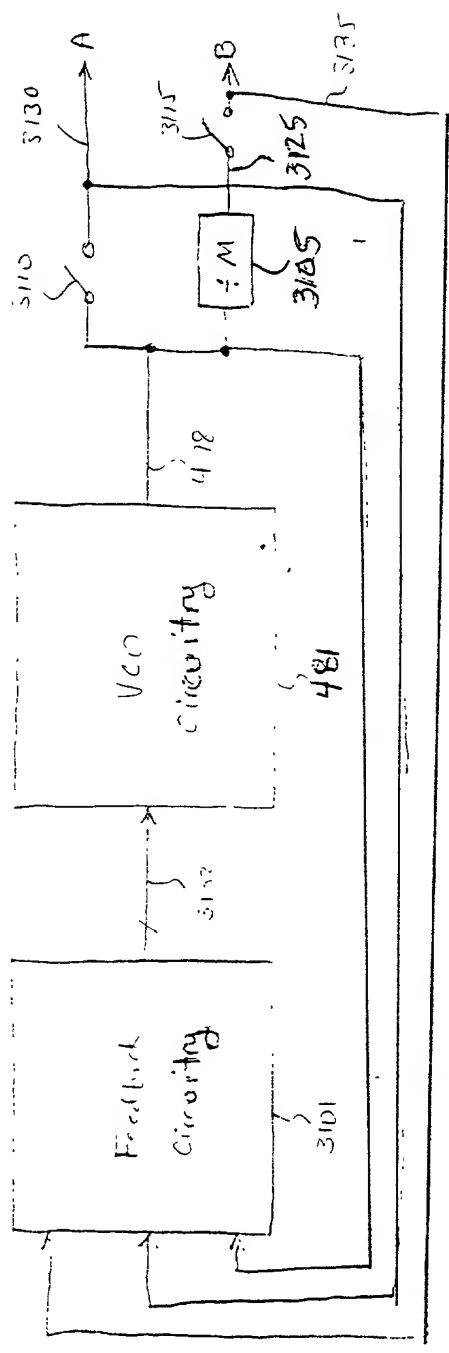
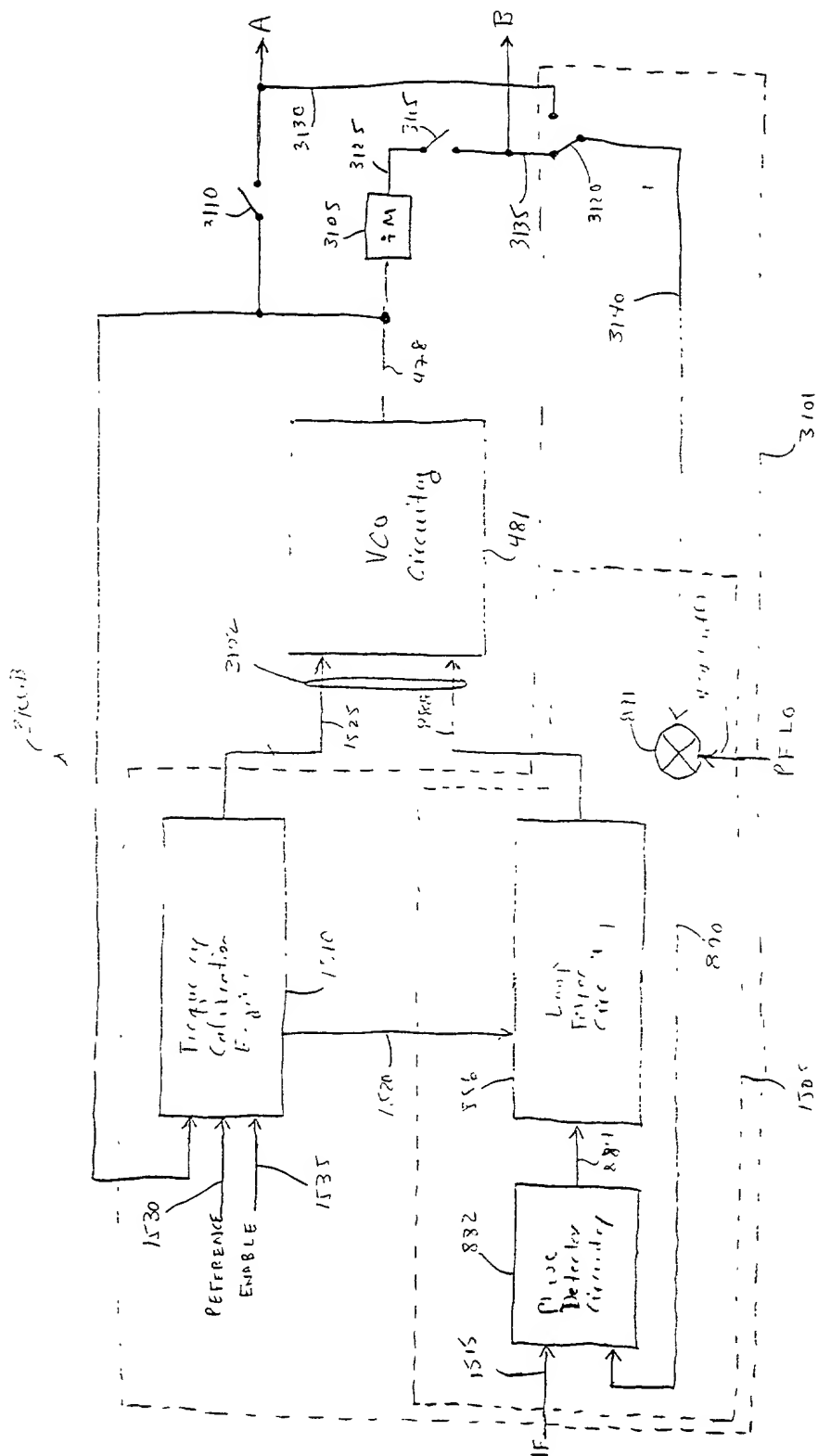


FIG. 1A

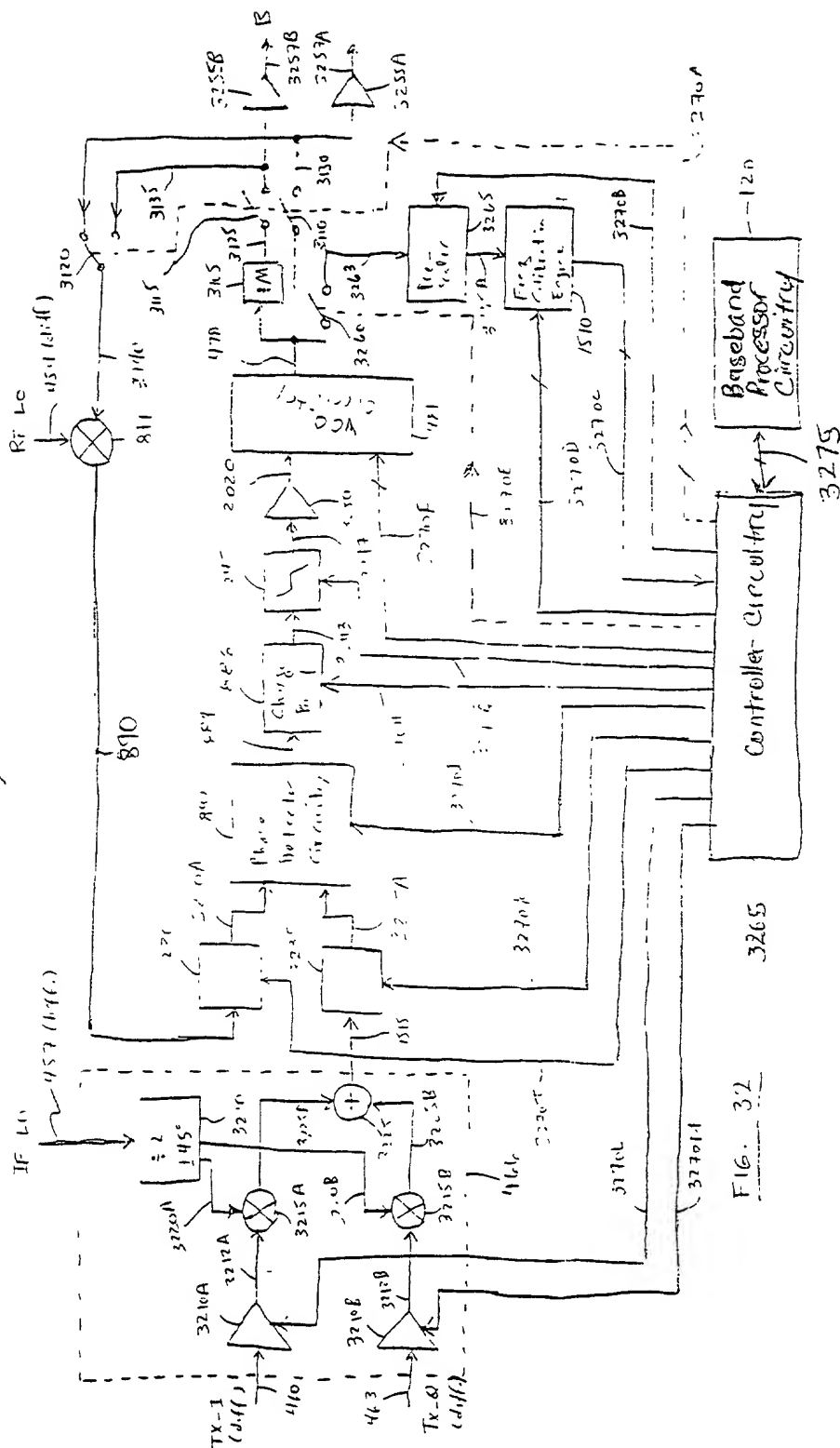
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3-100



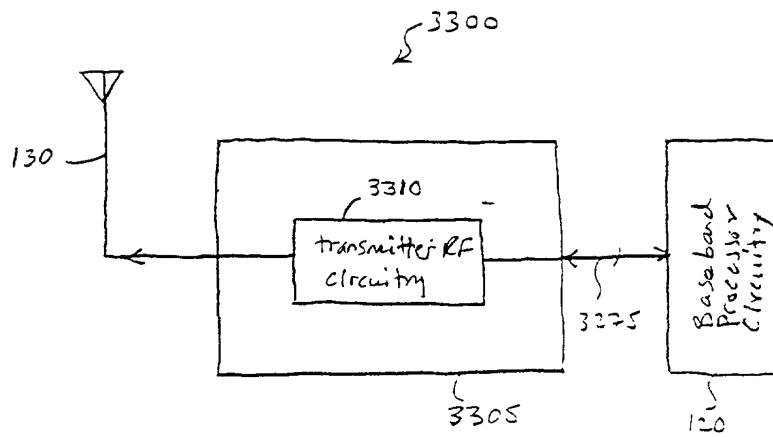


FIG. 33

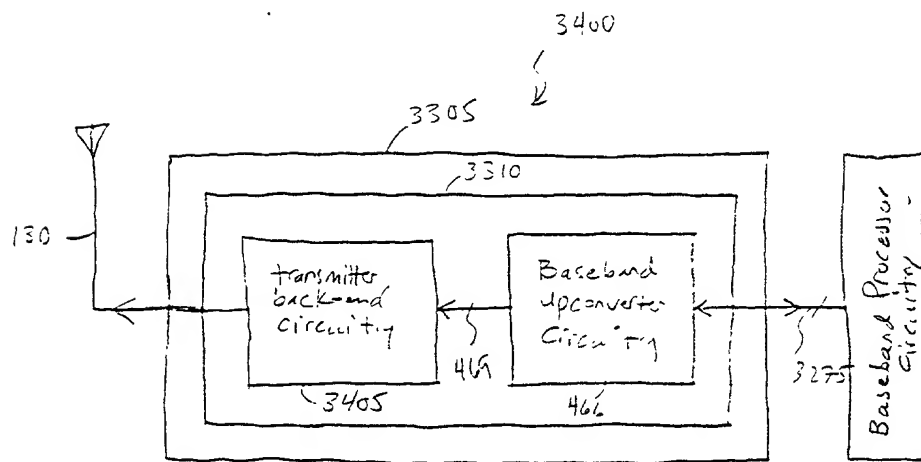


FIG. 34

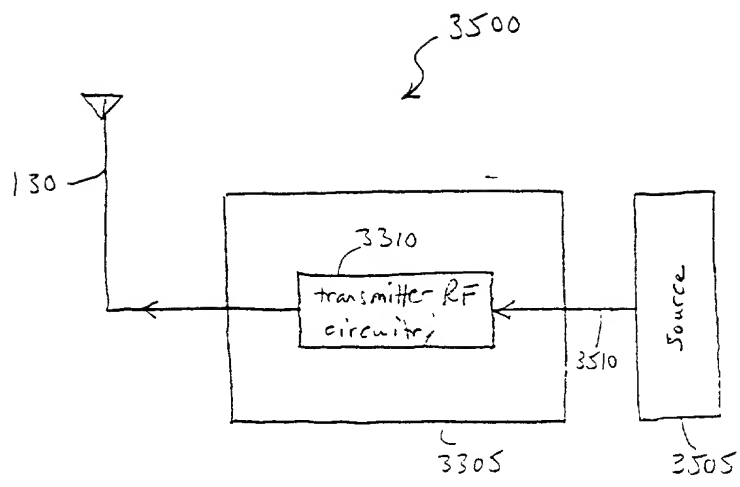


FIG. 35

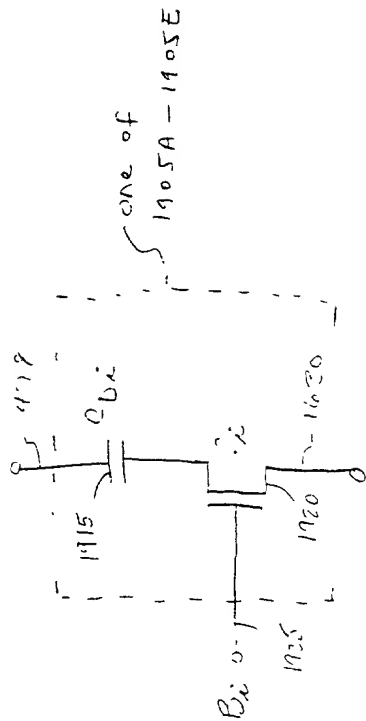


FIG. 13B